

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1	(703/24.ccls. 703/25.ccls. 703/26.ccls. 703/27.ccls. 703/28.ccls.) and (in adj system adj programming)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 15:27
L3	22	"714".clas. and (in adj system adj programming)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 15:28
L4	2	3 and emulat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:05
L5	3	"326".clas. and (key adj code)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:06
L6	2	"326".clas. and ((program\$5 adj mode) same key)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:07
L7	0	"716".clas. and ((program\$5 adj mode) same key)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:07
L8	14	"713".clas. and ((program\$5 adj mode) same key)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:10
L9	5	"713".clas. and ((program\$5 adj mode) same key) and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:23
L10	2	"5182810".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:23

L11	94	microcontroller and (security adj2 key) and (program adj mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:37
L12	0	11 and "703".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:24
L13	4	11 and "713".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:24
L14	0	11 and "326".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:24
L15	0	11 and "714".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:24
L16	0	11 and "716".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:24
L17	336	microcontroller and (unlock same key)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:37
L18	19	17 and (program adj mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 16:37
S2	2	"5590354".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 09:14
S3	2	"6058263".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 10:12

S5	2	"5752013".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 11:21
S6	2	"6,594,802".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 13:13
S7	74	psoc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 13:14
S9	0	S7 and pod	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 13:14
S10	1686	pod and ice	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 13:14
S11	41	S10 and emulat\$9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 16:02
S12	1686	pod and ice	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 16:03
S13	41	S12 and emulat\$9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 16:03
S14	1	S13 and (lock\$step)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 16:03
S15	1	S13 and (lock same step)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 16:03

S16	6	S13 and (step same (execute execution))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 16:13
S17	5	jeeni and jtag	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 16:25
S18	227	(soc same designer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 16:25
S19	14	S18 and (emulation emulates emulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 16:51
S20	2	"20020091991"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 17:22
S21	1	"60203575"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/09 17:26
S22	4	pod and emulator and isp	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 09:21
S23	16	ice and emulator and isp	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 09:21
S24	15	S23 not S22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 09:21

S25	15	("20020007264" "20020062461" "20020099977" "5313618" "5802347" "5954824" "5960190" "5964890" "6094729" "6167365" "6233673" "6516428" "6523148" "6571356" "6598176").PN. OR ("6829727"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:28
S26	1	"6691251".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:49
S27	0	S25 and (lock\$step)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:51
S28	15	"714".clas. and (ice and pod and emulat\$9)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:53
S29	1	S28 and (lock\$step)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:53
S30	255	"714".clas. and (lock\$step)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:53
S31	32	S30 and emulat\$9	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:55
S32	2	S31 and fpga	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:54
S33	6	S31 and ice	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:55
S34	1	S31 and pod	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:55
S35	25	S31 not S32 not S33 not S34	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:58
S36	168	714/28.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:59
S37	33	714/28.ccls. and ice	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 09:59
S38	2	714/28.ccls. and (lock\$step)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 10:00

S39	23	714/28.ccls. and (microcontroller and emulat\$9)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 10:05
S40	33	714/30.ccls. and (microcontroller and emulat\$9)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 10:05
S41	30	S40 not S39	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 13:24
S42	24	("703".clas. "714".clas.) and (ICE and POD)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 13:37
S43	0	(emulat\$9 and programmer).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 13:37
S44	54	(emulat\$9 and program\$4).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/10 13:37
S45	40	S44 and ("703".clas. "714".clas. "716".clas.)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:21
S46	1	"6223144".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:25
S47	1	"5493723".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:25
S48	1	"5999725".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:26
S49	1	"6161199".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:32
S50	1	"6185522".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:27
S51	1	"6263484".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:35
S52	1	"6289300".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:36
S53	1	"6298320".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:38
S54	1	"6366878".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:44

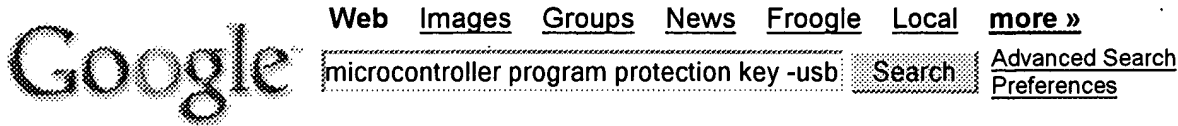
S55	505	(master same slave same microcontroller)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:45
S56	3	S55 same (emulation emulator emulates)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:45
S57	1	"5889988".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:46
S58	1	"6516428".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:55
S59	0	(in adj system adj program\$4) and (in adj circuit adj emulat\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:55
S60	290	(ISP and ICE and SOC)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:55
S61	0	S60 and microcontroller	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:55
S62	0	S60 and emulator	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 12:55
S63	47	S60 and computer	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:07
S64	1	"6487700".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:13
S65	1	"5559996".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:14
S66	1	"6223272".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:14
S67	1	"5964893".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:18
S68	1	"6107826".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:19
S69	1	"6032268".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:19

S70	2	"5805792".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 13:19
S71	1	jones.xa. and cypress.as.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:50
S72	0	thomson.xa. and cypress.as.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:27
S73	351	(in adj system adj program\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:50
S74	3	S73 and (ice)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 13:51
S75	5	S73 and emulator	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 14:53
S76	3	(microcontroller same cloning)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 14:54
S77	29	(microcontroller same (reverse adj engineer\$4))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/11 14:56
S78	19	(microcontroller and fpga and (reverse adj engineer\$4) and (logic adj (analyzer analyser)))	US-PGPUB; USPAT; USOCR	OR	ON	2005/05/11 15:03
S79	34	(microcontroller and fpga and (logic adj (analyzer analyser)) and (emulator emulation emulates))	US-PGPUB; USPAT; USOCR	OR	ON	2005/05/11 15:04
S80	34	S79 not S78	US-PGPUB; USPAT; USOCR	OR	ON	2005/05/11 15:35
S81	118	(train adj algorithm)	US-PGPUB; USPAT; USOCR	OR	ON	2005/05/11 15:35
S82	1	emulation and cloner and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 16:18
S83	245	emulation and programmer and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 16:20

S84	3	S83 and pod	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 16:18
S85	0	(microcontroller adj clone)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 16:20
S86	1	(pic adj clone)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 17:00
S87	0	(soc adj emulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 17:00
S88	13	(soc same emulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 17:08
S89	2	"4868822".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 17:05
S90	0	(soc and (fpga same pod) and (microcontroller same (target dut uut)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 17:08
S91	0	((fpga same pod) and (microcontroller same (target dut uut)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 17:09
S92	18	((fpga same emulat\$5) and (microcontroller same (target dut uut)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 17:13
S93	16	(microcontroller adj emulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 17:24

S94	2	"6347395".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 17:24
S95	2	thomson.xa. and "8051"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/12 09:14
S96	29	("4992934" "5283900" "5291614" "5313618" "5329471" "5438672" "5442789" "5450607" "5481684" "5481693" "5574927" "5630153" "5748468" "5761516" "5781792" "5790824" "5799169" "5819067" "5872993" "6032247" "6052773" "6163764" "6185522" "6185628" "6223274" "6256725" "6272452" "6308255" "6374312").PN. OR ("6564179"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/12 11:00
S97	1	"6173419".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/12 11:16
S98	34	(trace adj driven adj simulation)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/12 11:20
S99	12	(real adj time) and (trace adj driven adj simulation)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/12 11:25
S10 1	1	"5109353".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/12 11:33
S10 2	1	"5663900".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/12 13:16
S10 3	134	(in adj system adj programming)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/12 13:16
S10 4	32	S103 and microcontroller	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/12 13:20
S10 5	1	S103 and 703/28.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/12 13:20

S10 6	2	S103 and "703".clas.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/12 13:20
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**Web**

Results 11 - 20 of about 27,400 for microcontroller program protection key -usb. (0.12 seconds)

[PDF] Implementing Wake-up on Key Stroke

File Format: PDF/Adobe Acrobat

... if enabled, will wake the **microcontroller** from SLEEP. In ... when a **key** is pressed. IMPLEMENTATION. Figure 1 depicts an application where four keys are ...
ww1.microchip.com/downloads/en/AppNotes/00552e.pdf - [Similar pages](#)

Adapt9S12E128 Microcontroller Module

... The flexible design of the entire "Adapt" series **microcontroller** products accomodates ... multiple PWM channels with fault **protection** and current sense ...
www.technologicalarts.com/myfiles/AD9S12E128.html - 27k - [Cached](#) - [Similar pages](#)

Serial Keypads Made Easy With PicBasic Pro

... The circuit for the **microcontroller/key** connections is shown below in figure ... wait will not let **program** execution proceed until you release the **key**. ...
www.rentron.com/PicBasic/Serial-Keys.htm - 28k - [Cached](#) - [Similar pages](#)

Introduction to microcontroller PIC16F870 and PIC16F84

... The **program** is a set of instruction that the **microcontroller** will perform for you. ... Code **Protection** Data Enable; Flash **Program** Memory Write Enable ...
hem.passagen.se/communication/870.html - 16k - [Cached](#) - [Similar pages](#)

SBC MC68HC908

... MC68HC11) worlds most popular **microcontroller** family. ... professional quality and Free software tools, application notes, and powerful 3rd party apps, ...
www.vikon.com/sbchc908.htm - 46k - [Cached](#) - [Similar pages](#)

CS294-11 Spring 1999 Project - Palm to COTS Dust IrDA link

... If use oscillator, only need one for both **microcontroller** and UART ...
Memory **Protection** Devices, PC Mount Coin Cell Holder, Digi-Key page 499 (BH800) ...
www-bsac.eecs.berkeley.edu/archive/users/warneke-brett/post-pc/ - 21k - [Cached](#) - [Similar pages](#)

Microcontroller Enables Digital Control in SMPS

... **Protection** circuitry within the power device avoids shoot-through, ...
Microcontroller manufacturers offer detailed application notes on their Web sites ...
powerelectronics.com/mag/power_microcontroller_enables_digital/ - 50k - [Cached](#) - [Similar pages](#)

F Talker Loading

... a problem occurs that corrupts **microcontroller** memory, the talker **program** ...
Now, the EEPROM write **protection** must be overridden. Type the following: ...
www.me.unm.edu/~dgeiger/AppF.htm - 12k - May 10, 2005 - [Cached](#) - [Similar pages](#)

STMicroelectronics - ST10 Embedded Flash Memory Microcontrollers ...

... ST10F280 Embedded Flash Memory **Microcontroller** ... ST10F280 **Key** Features ...
When set, the **protection** can be reset only by programs legally executing ...
www.st.com/stonline/prodpres/dedicate/auto/embedded/st10f280.htm - 28k - [Cached](#) - [Similar pages](#)

[PDF] Using the HC08 Microcontroller Family to Enhance System Security

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... a **microcontroller** unit (MCU) with embedded. Flash memory featuring memory lock

... **Key Benefits.** > Add security to hardware and. firmware designs ...

[www.freescale.com/files/ shared/doc/selector_guide/SG2093.pdf](http://www.freescale.com/files/shared/doc/selector_guide/SG2093.pdf) - [Similar pages](#)

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microcontroller program mode

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Results 1 - 10 of about 150,000 for **microcontroller program mode**. (0.28 seconds)

Multi Chip Programmer

... to **program** the 8-pin PIC12c508A and 18pin PIC16F84 **microcontroller** chips to ... MCLR pin (between 12v and 14v) to tell the chip to go into **program mode**. ...
www.talkingelectronics.com/FreeProjects/MultiChipPgm/MultiChipPgm-P1.html - 24k - [Cached](#) - [Similar pages](#)

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[The Microcontroller Shop](#)
 Emulators, Debuggers, Programmers
 for all major micro architectures
microcontrollershop.com

PIC Microcontroller PIC Start Plus Programmer Using the Picstart ...

... does not release you from possibility to go into unwilling **program mode**. ... solenoid in my application sometime push PIC into undefined **mode** because of ...
www.piclist.com/techref/microchip/picstartplusisp.htm - 17k - [Cached](#) - [Similar pages](#)

Kanda - AVR ST7 COP8 PIC Microcontroller Programmer

UK based distributors of PIC **microcontroller programming** devices. ... User selectable Plug and **Program mode** allows automatic **programming** start when the ...
www.kanda.com/productnews.php3 - 25k - May 10, 2005 - [Cached](#) - [Similar pages](#)

PIC microcontrollers : chapter 2 - Microcontroller PIC16F84

... an 8-bit register inside a **microcontroller** that works independently of the **program**. ... Pin no.12 RB6 Sixth pin on port B. 'Clock' line in **program mode**. ...
www.mikroelektronika.co.yu/english/product/books/PICbook/2_01chapter.htm - 20k - [Cached](#) - [Similar pages](#)

Equinox Products Page - FS2003 - Portable ISP Programmer for 8051 ...

... custom logic on the Target System in order to enter Serial **Programming Mode** ...
 The FS2003 supports **programming** of the Atmel ATmega AVR **Microcontroller** ...
www.equinox-tech.com/products/details.asp?ID=370 - 41k - [Cached](#) - [Similar pages](#)

Equinox Products Page - FS2000A(AS)

... In-System **Programming** (ISP) Solution for Atmel AVR and AT89S **Microcontrollers** ... The **programmer** can be utilised as standard in 'Standalone **Mode**' ie not ...
www.equinox-tech.com/products/details.asp?ID=154 - 33k - [Cached](#) - [Similar pages](#)

S

... the required code file to **microcontroller** for **programming microcontroller**. ... the **program** on **microcontroller** put the RESET JUMPER[JP1] in run **mode** and ...
chaokhun.kmitl.ac.th/~kswichit/s52/s52slb.htm - 6k - [Cached](#) - [Similar pages](#)

Atmel Flash Micro Programmer

ATMEL 8051 Flash Based-**Microcontroller Programmer** DIY version 1.1 ... be individually **programmed** in 2 groups of 12 and used in 3 major **modes** of operation. ...
chaokhun.kmitl.ac.th/~kswichit/shiao/prog.htm - 14k - [Cached](#) - [Similar pages](#)

[PDF] Programming Flash Microcontrollers through the Controller Area ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... Figure 1. **Programming a Flash Microcontroller** through the CAN Interface ...
bootloader enters a flash **program monitor mode** and a prompt ...
www.fujitsu.com/downloads/MICRO/fma/pdf/canproflash.pdf - [Similar pages](#)

Background Debug Mode Speeds Up 16-Bit Microcontroller Application ...
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



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